

Hspice User Guide

Yicheng Fang

Avant, Star-Hspice User's Manual Meta-Software, Inc,1996

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HSPICE Users' Manual Meta-Software, Inc,1987

HSPICE ,1992

MOSFET Modeling & BSIM3 User's Guide Yuhua Cheng, Chenming Hu, 2007-05-08 Circuit simulation is essential in integrated circuit design, and the accuracy of circuit simulation depends on the accuracy of the transistor model. BSIM3v3 (BSIM for Berkeley Short-channel IGFET Model) has been selected as the first MOSFET model for standardization by the Compact Model Council, a consortium of leading companies in semiconductor and design tools. In the next few years, many fabless and integrated semiconductor companies are expected to switch from dozens of other MOSFET models to BSIM3. This will require many device engineers and most circuit designers to learn the basics of BSIM3. *MOSFET Modeling & BSIM3 User's Guide* explains the detailed physical effects that are important in modeling MOSFETs, and presents the derivations of compact model expressions so that users can understand the physical meaning of the model equations and parameters. It is the first book devoted to BSIM3. It treats the BSIM3 model in detail as used in digital, analog and RF circuit design. It covers the complete set of models, i.e., I-V model, capacitance model, noise model, parasitics model, substrate current model, temperature effect model and non quasi-static model. *MOSFET Modeling & BSIM3 User's Guide* not only addresses the device modeling issues but also provides a user's guide to the device or circuit design engineers who use the BSIM3 model in digital/analog circuit design, RF modeling, statistical modeling, and technology prediction. This book is written for circuit designers and device engineers, as well as device scientists worldwide. It is also suitable as a reference for graduate courses and courses in circuit design or device modelling. Furthermore, it can be used as a textbook for industry courses devoted to BSIM3. *MOSFET Modeling & BSIM3 User's Guide* is comprehensive and practical. It is balanced between the background information and advanced discussion of BSIM3. It is helpful to experts and students alike.

Users Guide SPICE Version 2G.0. Reprint E. Lindberg, DTH., 1982

SPICE Version 2G.1 User's Guide Andrei Vladimirescu,1980

Continuous System Modeling François E. Cellier, Jürgen Greifeneder, 2013-03-14 Modeling and Simulation have become endeavors central to all disciplines of science and engineering. They are used in the analysis of physical systems where they help us gain a better understanding of the functioning of our physical world. They are also important to the design of new engineering systems where they enable us to predict the behavior of a system before it is ever actually built. Modeling and simulation are the only techniques available that allow us to analyze arbitrarily non-linear systems accurately and under varying experimental conditions. Continuous System Modeling introduces the student to an important subclass of these techniques. They deal with the analysis of systems described through a set of ordinary or partial differential equations or through a set of difference equations. This volume introduces concepts of modeling physical systems through a set of differential and/or difference equations. The purpose is twofold: it enhances the scientific understanding of our physical world by codifying (organizing) knowledge about this world, and it supports engineering design by allowing us to assess the consequences of a particular design alternative before it is actually built. This text has a flavor of the mathematical discipline of dynamical systems, and is strongly oriented towards Newtonian physical science.

Practical RF Amplifier Design and Performance Optimization with SPICE and Load- and Source-pull

Techniques Amal Banerjee, 2021-03-01 This book explains and demonstrates with an exhaustive set of design examples, how common types of radio frequency (RF) amplifiers (classes A, B, AB, C, D, E, F, G and H) can be designed, and then have their performance characteristics evaluated and optimized with SPICE. The author demonstrates the transient analysis features of SPICE, along with industry-standard load- and source-pull techniques to simulate the steady-state, long-term time-domain behavior of any test RF amplifier. Describes methods for designing and evaluating/optimizing the performance characteristics of an RF amplifier that circumvent the issues involved with existing, traditional methods and don't require expensive, high-end software tools; Includes C language executables for each RF amplifier type, eliminating errors that might creep in while computing passive component (capacitor, inductor, resistor) values for a given RF amplifier type; Demonstrates industry-standard load- and source-pull schemes that can be included easily in text SPICE netlists, allowing accurate calculation of impedance matching and impedance values at the input and output ports of the test RF amplifier, eliminating messy, error-prone S parameter based calculations.

SPICE Version 2F.1 A. R. Newton, Donald O. Pederson, A. Vladimirescu, 1985

Power Distribution Network Design for VLSI Qing K. Zhu, 2004-02-19 A hands-on troubleshooting guide for VLSI network designers The primary goal in VLSI (very large scale integration) power network design is to provide enough power lines across a chip to reduce voltage drops from the power pads to the center of the chip. Voltage drops caused by the power network's metal lines coupled with transistor switching currents on the chip cause power supply noises that can affect circuit

timing and performance, thus providing a constant challenge for designers of high-performance chips. Power Distribution Network Design for VLSI provides detailed information on this critical component of circuit design and physical integration for high-speed chips. A vital tool for professional engineers (especially those involved in the use of commercial tools), as well as graduate students of engineering, the text explains the design issues, guidelines, and CAD tools for the power distribution of the VLSI chip and package, and provides numerous examples for its effective application. Features of the text include: * An introduction to power distribution network design * Design perspectives, such as power network planning, layout specifications, decoupling capacitance insertion, modeling, and analysis * Electromigration phenomena * IR drop analysis methodology * Commands and user interfaces of the VoltageStorm(TM) CAD tool * Microprocessor design examples using on-chip power distribution * Flip-chip and package design issues * Power network measurement techniques from real silicon The author includes several case studies and a glossary of key words and basic terms to help readers understand and integrate basic concepts in VLSI design and power distribution.

Nanoelectronic Device Applications Handbook James E. Morris, Krzysztof Iniewski, 2017-11-22 Nanoelectronic Device Applications Handbook gives a comprehensive snapshot of the state of the art in nanodevices for nanoelectronics applications. Combining breadth and depth, the book includes 68 chapters on topics that range from nano-scaled complementary metal-oxide-semiconductor (CMOS) devices through recent developments in nano capacitors and AlGaAs/GaAs devices. The contributors are world-renowned experts from academia and industry from around the globe. The handbook explores current research into potentially disruptive technologies for a post-CMOS world. These include: Nanoscale advances in current MOSFET/CMOS technology Nano capacitors for applications such as electronics packaging and humidity sensors Single electron transistors and other electron tunneling devices Quantum cellular automata and nanomagnetic logic Memristors as switching devices and for memory Graphene preparation, properties, and devices Carbon nanotubes (CNTs), both single CNT and random network Other CNT applications such as terahertz, sensors, interconnects, and capacitors Nano system architectures for reliability Nanowire device fabrication and applications Nanowire transistors Nanodevices for spintronics The book closes with a call for a new generation of simulation tools to handle nanoscale mechanisms in realistic nanodevice geometries. This timely handbook offers a wealth of insights into the application of nanoelectronics. It is an invaluable reference and source of ideas for anyone working in the rapidly expanding field of nanoelectronics.

Analog Circuit Design Johan Huijsing, Rudy J. van de Plassche, Willy M.C. Sansen, 2013-03-14 Johan H. Huijsing This book contains 18 tutorial papers concentrated on 3 topics, each topic being covered by 6 papers. The topics are: Low-Noise, Low-Power, Low-Voltage Mixed-Mode Design with CAD Tools Voltage, Current, and Time References The papers of this book were written by top experts in the field, currently working at leading European and American universities and companies. These

papers are the reviewed versions of the papers presented at the Workshop on Advances in Analog Circuit Design, which was held in Villach, Austria, 26-28 April 1995. The chairman of the Workshop was Dr. Franz Dielacher from Siemens, Austria. The program committee consisted of Johan H. Huijsing from the Delft University of Technology, Prof. Willy Sansen from the Catholic University of Leuven, and Dr. Rudy van der Plassche from Philips Eindhoven. This book is the fourth of a series dedicated to the design of analog circuits. The topics which were covered earlier were: Operational Amplifiers Analog to Digital Converters Analog Computer Aided Design Mixed A/D Circuit Design Sensor Interface Circuits Communication Circuits Low-Power, Low-Voltage Integrated Filters Smart Power As the Workshop will be continued year by year, a valuable series of topics will be built up from all the important areas of analog circuit design. I hope that this book will help designers of analog circuits to improve their work and to speed it up.

SPICE Version 2G.5, 1981

SPICE Version 2G.5.0 User's Guide Data General Corporation, Lawrence Nagel, Ellis Cohen, 1982

Top-Down Design of High-Performance Sigma-Delta Modulators Fernando Medeiro, Belén Pérez Verdú, Angel Rodríguez-Vázquez, 2013-04-18 The interest for $\Sigma\Delta$ modulation-based NO converters has significantly increased in the last years. The reason for that is twofold. On the one hand, unlike other converters that need accurate building blocks to obtain high resolution, $\Sigma\Delta$ converters show low sensitivity to the imperfections of their building blocks. This is achieved through extensive use of digital signal processing - a desirable feature regarding the implementation of NO interfaces in mainstream CMOS technologies which are better suited for implementing fast, dense, digital circuits than accurate analog circuits. On the other hand, the number of applications with industrial interest has also grown. In fact, starting from the earliest in the audio band, today we can find $\Sigma\Delta$ converters in a large variety of NO interfaces, ranging from instrumentation to communications. These advances have been supported by a number of research works that have led to a considerably large amount of published papers and books covering different sub-topics: from purely theoretical aspects to architecture and circuit optimization. However, so much material is often difficultly digested by those unexperienced designers who have been committed to developing a $\Sigma\Delta$ converter, mainly because there is a lack of methodology. In our view, a clear methodology is necessary in $\Sigma\Delta$ modulator design because all related tasks are rather hard.

SPICE Version 2G User's Guide, 1981

The SPICE Book Andrei Vladimirescu, 1994 This new book, written by Andre Vladimirescu, who was instrumental in the development of SPICE at the University of California Berkeley, introduces computer simulation of electrical and electronics circuits based on the SPICE standard. Relying on the functionality first supported in SPICE2 that is now supported in all SPICE programs, this text is addressed to all users of electrical simulation. The approach to learning circuit simulation is to interpret simulation results in relation to electrical engineering fundamentals; the book asks the student to solve most circuit

examples by hand before verifying the results with SPICE. Addressed to both the SPICE novice and the experienced user, the first six chapters provide the relevant information on SPICE functionality for the analysis of linear as well as nonlinear circuits. Each of these chapters starts out with a linear example accessible to any new user of SPICE and proceeds with nonlinear transistor circuits. The latter part of the book goes into more detail on such issues as functional and hierarchical models, distortion analysis, basic algorithms in SPICE and related options parameters, and, how to direct SPICE to find a solution when it does not converge to a solution. The approach emphasizes that SPICE is not a substitute for knowledge of circuit operation but a complement. The SPICE Book is different from previously published books in the approach of solving circuit problems with a computer. The solution to most circuit examples is sketched out by hand first and followed by a SPICE verification. For more complex circuits it is not feasible to find the solution by hand but the approach stresses the need for the SPICE user to understand the results. Readers gain a better comprehension of SPICE thanks to the importance placed on the relation between EE fundamentals and computer simulation. The tutorial approach advances from the hand solution of a circuit to SPICE verification and simulation results interpretation. This book teaches the approach to electrical circuit simulation rather than a specific simulation program. Examples are simulated alternatively with SPICE2, SPICE3 or PSPICE. Accurate descriptions, simulation rationale and cogent explanations make this an invaluable reference.

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